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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/061, 671

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Inventor(s):

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Examiner: Wang, Albert C.

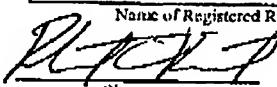
Group/Art Unit: 2115

Atty. Dkt. No: 5500-68500

Title: Apparatus and Method for
Selective Clock Control to
Reduce Power Consumption
in an Integrated Circuit**** CERTIFICATE OF FAXSIMILE ****
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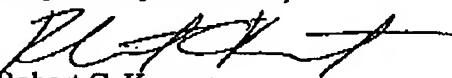
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Respectfully submitted,


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